

ARTEMIS TECHNOLOGY CONFERENCE 2016 INCLUDING H2020 PRE-BROKERAGE

4-6 October | Madrid, Spain

EMC² workshop – draft agenda 6 October 2016

1. Morning sessions

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|-------------|---|
| 9:00-9:30 | Overview of EMC2 project – achievements and challenges (Werner Weber) |
| 9.30-10:00 | System Architectures (Andreas Eckel) |
| 10:00-10.30 | Multi-core Hardware Architectures and Concepts (Rolf Meyer) |
| 10.30-11:00 | Coffee break |
| 11:00-11.30 | Demo of pilot usage of Framework, jointly with Arrowhead (Thomas Söderqvist, Volvo) |
| 11:30-12.00 | Executable Application Models and Design Tools for Mixed-Critical, Multi-Core Embedded Systems (Albert Cohen) |

2. Afternoon sessions

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| 13.30-14:00 | EMC2 towards standardization (Erwin Schoitsch) |
| 14.00-14:30 | Real Time Concept Prototyping and Validation of Functional Safety Certification Test Measures by FPGA platform (Zuhal Clarke) |
| 14:30-14.50 | Carrier Under Carrier Interference Detector (Javier Valera) |
| 14.50-15.10 | Industrial manufacturing and logistics (Juha Kuusela) |
| 15:10-15.30 | Internet of Things (Yudani Riobo) |