

# Asymmetric Multiprocessing on ZYNQ: ARM A9, MicroBlaze and 4x EdkDSP on Xilinx ZC702 board.

- ARM and MicroBlaze, both bare metal. Sharing DDR3 & terminal (based on the Xilinx ISE 14.5 ref. des. XAPP1093).
- MicroBlaze – Time critical aps.
- EdkDSP – Floating Point App. Spec. Acceleration. PicoBlaze6. Reconfigurable by firmware. Custom I/O. C compiler for PB.
- TODO for ARM A9 part: Linux, RTOS, LwIP, Ethernet, ...
- TODO for HW: Vivado 2013.4

Performance (MFLOPs)

